

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Rule 53(b) Divisional Application of S.N. 09/427,693:

Noboru YOKOTA et al.

Serial No.: NEW

Group Art Unit: **2826** (Expected)

Filed: **February 8, 2002**

Examiner: **DICKEY, T.** (Expected)

For: **SEMICONDUCTOR DEVICE HAVING EMBEDDED ARRAY**

INFORMATION DISCLOSURE STATEMENT
PURSUANT TO 37 CFR 1.97(b)

Commissioner for Patents
Washington, D.C. 20231

February 8, 2002

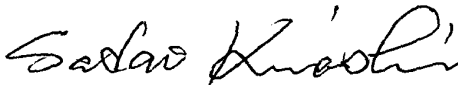
Sir:

This Information Disclosure Statement is being filed in order to comply with Applicant's duty of disclosure under 37 CFR 1.56. The documents listed on the Form PTO-1449 were made of record in parent application Serial No. 09/427,693.

The Commissioner is authorized to charge our Deposit Account No. 01-2340 for any fee which is deemed by the Patent and Trademark Office to be required to effect consideration of this statement.

Respectfully submitted,

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Enclosures: PTO-1449

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	Applicants: Noboru YOKOTA et al.	
	Filing Date: February 8, 2002	Group Art Unit: 2826 (Expected)

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 U.S. PTO
 02/08/02

U.S. PATENT DOCUMENTS

Examiner Initial		Document No.	Name	Date	Class	Subclass	Filing Date (If appropriate)
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_____	AB	5,677,641	Nishio et al.	10/14/1997	326	121	
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OTHER DOCUMENTS

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<table border="1" style="width: 100%;"> <tr> <td style="width: 35%;">Examiner</td> <td style="width: 65%;">Date Considered</td> </tr> </table>			Examiner	Date Considered
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